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				Application Number	10/643338
				Filing Date	8/19/2003
				First Named Inventor	Thomas C. McDonald
				Group Art Unit	2183
				Examiner Name	Brian P. Johnson
Sheet	1	of	1	Attorney Docket Number	CNTR.2142

U.S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

Examiner Signature	/Brian Johnson/	Date Considered	07/26/2006
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U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ^{2(a) known}			
BJ		6101595	08-08-2000	Pickett et al.	
BJ		6157988	12-05-2000	Eric M. Dowling	
BJ		6968444	11-22-2005	Kroesche et al.	
BJ		6725357	04-20-2004	Jean Philippe Cousin	
BJ		6041405	03-21-2000	Thomas S. Green	
BJ		6754808	06-22-2004	Roth et al.	
BJ		5434985	07-18-1995	Emma et al	
BJ		5553246	09-03-1996	Hiromi Suzuki	
BJ		6122729	09-19-2000	Thang M. Tran	
BJ		6748441	06-08-2004	David James Gemmell	
BJ		6321321	11-20-2001	Mark W. Johnson	

NON PATENT LITERATURE DOCUMENTS					
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BJ		Online Computing Dictionary. http://instantweb.com/d/dictionary/foldoc.cgi?query=btb 05/05/1995.	Branch Target Buffer		
BJ		The D Latch, Play-Hockey Web Page, Oct 10, 1999.			
BJ		Jimenez et al. "The Impact of Delay on the Design of Branch Predictors." 2000			
BJ		EBERLY et al. "The Correlation Branch Target Address Cache" May 1996.			
BJ		Patterson et al. "Computer Organization & Design: The Hardware/Software Interface." Morgan Kaufmann Publishers, Inc. San Francisco, CA. 1998 Page 453-455			

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